An Institute of National Importance Specialized in IT Enabled Design and Manufacturing Chennai - 600127

Advertisement for the Post of Junior Research Fellow

Advt. No.: IIITDMK/PR/JRF/A28/2024

Applications are invited for the following post. The post is purely on a contractual basis.

Name of Position	Junior Research Fellow (JRF)			
Number of Positions	One			
Nature of Work	To design, simulate, fabricate and test the metagratings based RIS			
Duration of the Position	The post is purely on a contractual basis and initially, the post is for 06 Months and can be extended for another 1.5 years.			
Stipend	Rs. 37,000/- pm			
Title of the Sponsored Project	Metagratings based Reconfigurable Intelligent Surfaces for X-band N-LOS Radar Surveillance			
Funding Agency	DRDO			
P.I / Co-P.I	Dr. Sreenath Reddy Thummaluru / Prof. M.V. Kartikeyan			
Department	Electronics and Communication Engineering			
Educational Qualification	BE/B.Tech in Electronics and Communication Engineering (or) relevant branch. AND			
	Master's degree in Electrical Engineering / Electrical and Electronics Engineering / Electronics Engineering / Electronics and Communication Engineering (or) Engineering in any area relevant to the project.			
Additional Qualification (Desirable, if any)	 Knowledge in RF & Microwave domain, antennas Expertise in MAT LAB, HFSS (or) CST 			
Age Limit	Preferably below 28 years as on Last date of receiving the application (Age relaxation is applicable as per Gol norms)			
Last date of receiving the application	13.11.2024			

Date: 23rd October 2024

How to Apply:

- 1. Interested candidates are requested to submit their applications in the enclosed prescribed format.
- 2. Take print of the application form, fill, sign, and scan the application form. Upload the scanned copy in google form link: https://forms.gle/VgWEhaQ9pK3rrHgC8
- 3. No need to send the hard copy.

Selection Procedure:

- 1. Candidates will be shortlisted based on their eligibility criteria, academic record, and relevant experience.
- 2. Only shortlisted candidates will be intimated through email for the written test and/or interview.
- 3. Candidates should be present physically at IIITDM Kancheepuram located at Chennai 600127 for the written test and/or interview.
- 4. Candidates should produce all educational and experience certificates at the time of interview for verification.
- 5. No TA/DA will be paid if called for an interview.

For any clarification regarding the application process, the candidate may contact the PI. email: sreenath@iiitdm.ac.in

Sd/x Dean (SR)

An Institute of National Importance Specialized in IT Enabled Design and Manufacturing Chennai - 600127

Appl. No.: (will be filled by Office)

APPLICATION FOR THE POSITION OF PROJECT STAFF

Candidates Photo
to be pasted here

1. Advertisement Number: 2. Name of the Post: 3. Candidate's Full Name (As per SSLC Mark sheet): Father's Name of the Candidate: 5. Date of Birth of the Candidate: 6. Nationality: 7. Gender: 8. Category (Gen / OBC / SC / ST): (For age relaxation only) 9. Address for Communication: 10. Permanent Address:

11. Contact Phone / Mobile Number(s):

- 12. Email Address:
- 13. Educational Qualifications Starting from Xth / SSLC:

SI. No.	Degree & Branch	Institute / University	Year of Entry & leaving	Percentage / CGPA & Class	Mode (Regular / Part-time / etc.)

14. Academic Record: GATE/ CSIR - NET qualification details with percent marks / Rank, year of qualifying, etc.:

a)		b)							
16	6. Are you currently working in Central / State / Semi-Govt / PSU / Autonomous Bodies of the Govt.? (If yes, please submit NOC issued by your present employer):								
17	.Pres	ent Position / Designation:							
18	18. Work Experience (in reverse chronological order including post held at present):								
	SI. No.	Organization	Designation	Start Date & End Date	Pay Scale / Total Pay				

15. Names and addresses of two referees along with phone numbers and e-mail

addresses:

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19. If selected, time required to join: